

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Manrique J. Brenes; Yen T. Nguyen  
Assignee: Cisco Technology, Inc.  
Title: METHOD AND SYSTEM FOR REAL-TIME BIT ERROR RATIO DETERMINATION  
Serial No.: 10/773,705 Filing Date: February 6, 2004  
Examiner: Stephen M. Baker Group Art Unit: 2133  
Docket No.: CIS0209US Confirmation No. 8310

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Austin, Texas  
June 21, 2007

MAIL STOP RCE  
COMMISSIONER FOR PATENTS  
P. O. BOX 1450  
ALEXANDRIA, VA 22313-1450

AMENDMENT AND REMARKS ACCOMPANYING  
REQUEST FOR CONTINUED EXAMINATION

Dear Sir:

This paper and the accompanying Request for Continued Examination (RCE) are being submitted in response to the Final Office Action dated March 21, 2007, having a shortened statutory period set to expire on June 21, 2007. Further examination and reconsideration are respectfully requested in view of the following amendments and remarks.

No Amendments to the Specification are presented in this paper.

No Amendments to the Drawings are presented in this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 9 of this paper.